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File 349:PCT FULLTEXT 1979-2002/UB=20030213,UT=20030123

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Set	Items	Description
S1	7	AU='HEIN JERREL P':AU='HEIN JERRELL PAUL'
S2	7	AU='SOOCH NAVDEEP S':AU='SOOCH NAVDEEP SINGH'
S3	2	S1 AND S2

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3/5,K/1 (Item 1 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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SUBSCRIBER LINE INTERFACE CIRCUITRY
TEILNEHMERLEITUNGSSCHNITTSTELLENSCHALTUNG
CIRCUITS D'INTERFACE DE LIGNE D'ABONNE

PATENT ASSIGNEE:

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US 5392334 A

US 5347577 A

GB 2032730 A

US 5636273 A

GOODENOUGH F: "SLIC EJECTS RELAYS FROM PABX LINE CARDS" ELECTRONIC
DESIGN,US,PENTON PUBLISHING, CLEVELAND, OH, vol. 42, no. 14, 11 July
1994 (1994-07-11), pages 55-56,58-59,62-, XP000459837 ISSN: 0013-4872;

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SUBSCRIBER LINE INTERFACE CIRCUITRY
CIRCUITS D'INTERFACE DE LIGNE D'ABONNE

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DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC
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SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG
(AP) GH GM KE LS MW SD SL SZ TZ UG ZW
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Detailed Description

Claims

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English Abstract

A subscriber line interface circuit apparatus includes a signal processor (210) having sense inputs for sensed tip and ring signals (222) of a subscriber loop (132). The signal processor (210) computes common mode and differential mode components of the subscriber loop. A linefeed driver portion (220) drives the subscriber loop in accordance with subscriber loop control signals (212) provided by the signal processor. The linefeed driver portion provides the sensed tip and ring signals. The sensed tip signal includes first (332) and second (334) sensed tip voltages. Differences between the first and second sensed tip voltages are proportional to a tip current. Similarly, the sensed ring signal includes first (336) and second (338) sensed ring voltages. Differences between the first and second ring voltages are proportional to a ring current. Numerous packaging variations are provided.

French Abstract

L'invention concerne un appareil a circuit d'interface de ligne d'abonne, qui comprend un processeur de signaux (210) ayant des entrees de lecture pour des signaux detectes de pointe et de nuque (222) d'une boucle d'abonne (132). Le processeur de signaux (210) calcule des composants de la boucle d'abonne de mode commun et de mode differentiel. Un circuit d'attaque d'alimentation (220) attaque la boucle d'abonne conformement aux signaux de commande (212) de la boucle d'abonne, fournis par le processeur de signaux. La partie de commande d'alimentation produit les signaux detectes de pointe et de nuque. Le signal de pointe detecte comporte une premiere (332) et une seconde (334) tension de pointe detectee. Les differences entre la premiere et la seconde tension de pointe detectee sont proportionnelles au courant de pointe. De facon analogue, le signal de nuque detecte comporte une premiere (336) et une seconde (338) tension de nuque detectee. Les differences entre la premiere et la seconde tension de nuque sont proportionnelles a un courant de nuque. L'invention concerne egalement de nombreuses possibilites d'assemblage.

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